

ABSTRACT OF THE DISCLOSURE

There are provided at least one read word line 15, 16 and 17 for transmitting a read control signal to a memory cell, at least one read bit line
5 18, 19 and 20 for transmitting information of the memory cell to an outside according to activation of the read control signal corresponding to the read word line, at least one write word line 11 and 12 for transmitting a write control signal to the memory cell, and at least one write bit line 13 and 14 for transmitting external information to the memory cell according to
10 activation of the write control signal corresponding to the write word line, wherein the read bit line and the write bit line are provided as alternately as possible and the read control signal and the write control signal are controlled so as not to be activated at the same time.